



May 5, 2013

San Jose, CA, May 5, 2013 - ASICSoft, a leader in engineering services, contractor placement services and product sales, together with VeriFast Technologies, the US leader in ASIC verification training programs, announced the start of VeriFast's most recent training classes – JumpStart.

JumpStart is a 500 hour ASIC Design Verification Training program. The target student for this program are new college grads looking for a JumpStart to their careers and Senior Logic Design engineers who are seeking a transition to Design Verification.

The JumpStart program is an intense, complex, and extremely hands-on ASIC Verification training program. During its four month session the average student will invest 500 hours of class instruction and exercises. Our students work with state-of-the-art EDA tools (Mentor Graphics QuestaSim).

- JumpStart provides training on verification topics such as:
- Functional Coverage
- Scripting and Regression
- Randomization and DPI
- SystemVerilog
- UVM
- Assertions
- Test Bench Development
- Many other important aspects of verification

“We are extremely excited with our partnership with VeriFast. Our services complete their training very well”, says Mike Chandler, ASICSoft CEO. “For the upcoming 2 classes we have enrolled more than 25 students”! Mr. Chandler added emphatically.